

PATENT NUMBER

U.S. UTILITY Patent Application

~~O.I.P.E~~

PATENT DATE -

SCANNED

Q.A

APPLICATION NO. 09/884517	CONT/PRIOR D	CLASS 363 257	SUBCLASS 369	ART UNIT 2836 3	EXAMINER DUONG
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APPLICANTS

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3744

CMOS inverter and integrated circuits utilizing strained silicon surface channel MOSFETs

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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